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APPLICANTS

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** CONTINUING DATA *****
None. sep

** FOREIGN APPLICATIONS *****
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY FRANCE	SHEETS DRAWING 5	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

Verified and
Acknowledged

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TITLE
 Method for manufacturing a multilayer semiconductor structure that includes an irregular layer

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